

# EUROPEAN PATENT OFFICE

## Patent Abstracts of Japan

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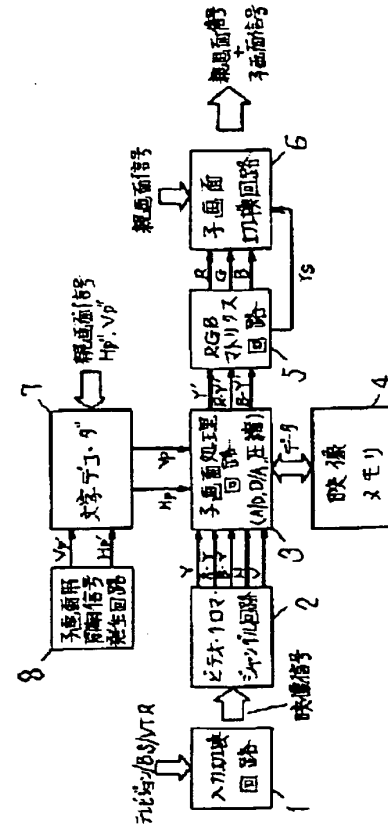
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TITLE : COLOR TELEVISION RECEIVER



**ABSTRACT :** PURPOSE: To enable the interlace display of a slave screen by using a synchronizing pulse from a synchronizing signal generating circuit for the slave screen for a clock for the slave screen at the time of a non-interlace display by a master screen.

**CONSTITUTION:** At the time of the non-interlace display such as a character display by the master screen, a character decoder 7 prepares the horizontal and vertical synchronizing pulses of the non-interlace by the incorporated synchronizing signal generating circuit. Then, this pulse is used for the display of the master screen, and the horizontal and vertical pulses outputted from a synchronizing signal generating circuit 8 for the slave screen are used for a clock for an A/D conversion, and the clock for reading a video memory 4, for preparing the slave screen. Then, the horizontal and vertical pulses are inputted through the decoder 7 to a slave screen processing circuit 3. Thus, at the time of the non-interlace display by the master screen, the interlace display by the slave screen can be operated.

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